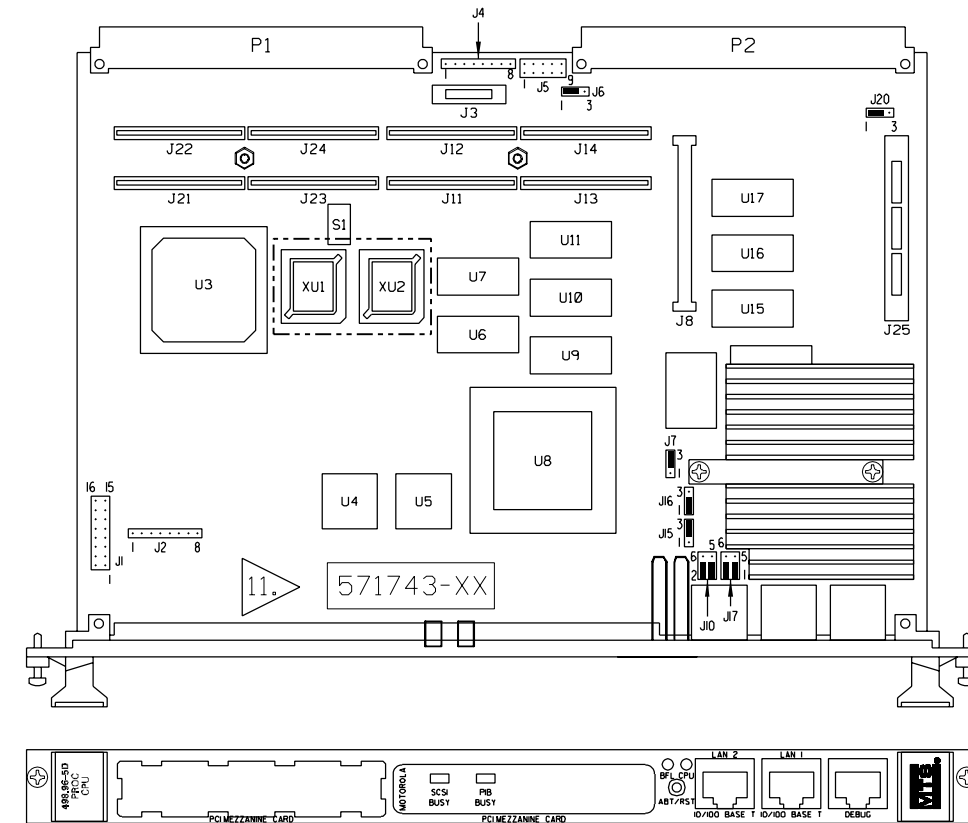
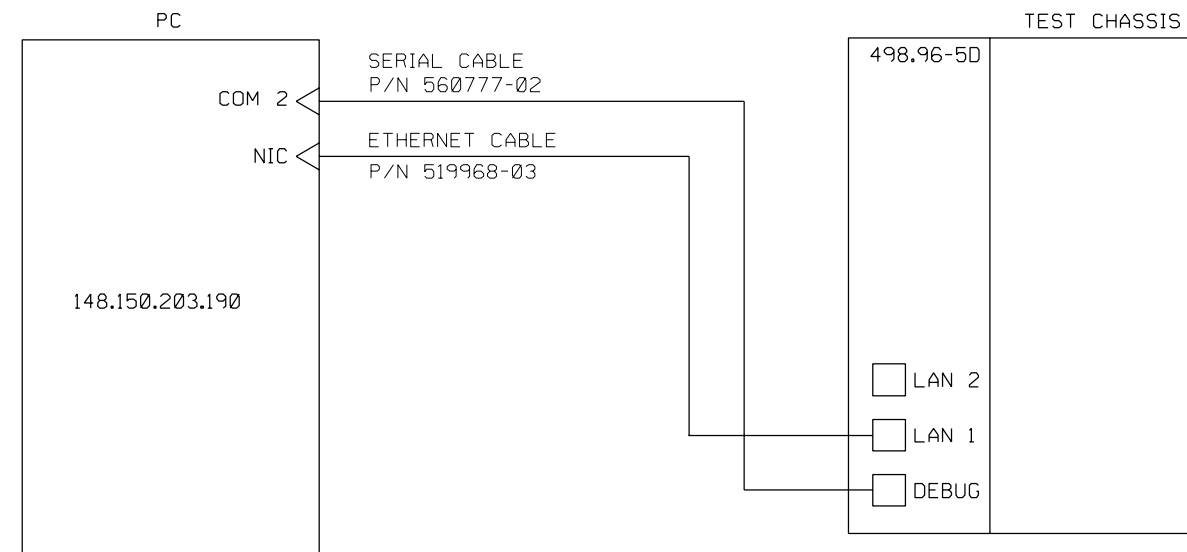


INSTRUCTIONS FOR PROGRAMMING PROCESSOR FLASH

1. USING PC WORKSTATION AND SOFTWARE PROVIDED, LAUNCH TFTP SERVER APPLICATION.
2. JUMPER PINS 1-2 ON HEADER J7 OF 498.96-5D PROCESSOR, AND INSTALL INTO SLOT 1 OF THE TEST CHASSIS.
3. ATTACH SERIAL CABLE (560777-02) TO COM 2 OF PC AND DEBUG PORT ON 498.96-5D AS SHOWN.
4. ATTACH ETHERNET CABLE (519968-03) BETWEEN PC AND 498.96-5D LAN 1 CONNECTOR AS SHOWN.
5. APPLY POWER TO VME CHASSIS AND WAIT 15 SECONDS.
6. LAUNCH IN-PLACE PROGRAMMER AND PRESS "START" BUTTON.
7. WHEN PROMPTED, SELECT A CONFIGURED PROCESSOR PART NUMBER FROM THE LIST PROVIDED.
8. WHEN PROGRAMMING IS COMPLETE, VERIFY CHECKSUM VALUE WITH CHECKSUM DOCUMENTATION PROVIDED WITH PROGRAMMER APPLICATION.
9. TURN OFF POWER TO VME CHASSIS AND REMOVE 498.96-5D.



10. JUMPER PINS 2-3 ON HEADER J7 OF 498-96-5D.



UNLESS OTHERWISE SPECIFIED						legitimately in the prior possession of the recipient.			
MATERIAL DESCRIPTION			.XXX HOLE SIZE TOLERANCE			DRAWN	CHECK	ENGR	MFG
MATERIAL SIZE			0.000 TO .750 +.010/- .002			OVER .750 TO 1.500 +.015/- .003	MRI	ARP	
							DATE	DATE	DATE
FINISH			MASK 	ANGLE ±2°		TITLE			
· THREAD DEPTHS ARE TO MIN FULL THDS · DRILL DEPTHS ARE TO FULL DIA · REMOVE BURRS AND SHARP EDGES · DO NOT SCALE PRINT			SCALE NONE	GENERAL TOLERANCES FRACTIONS DECIMALS .X ±.1 .XX ±.03 .XXX ±.010		PROCESSOR MODULE-498.96-5D, MVME 51105E, VX5.4			
 MTS SYSTEMS CORPORATION EDEN PRAIRIE, MINNESOTA U.S.A. ©			THIRD ANGLE PROJECTION 	NEXT LEVEL		SIZE	NUMBER	REV	
				PRODUCT CODE 498.96-5D			571743-XX	SEE TABLE	
						SHEET 2 OF			

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